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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**



**RECEIVED**

OCT 19 2004

**Technology Center 2100**

Serial No.: 09/760,752

Filed: January 17, 2001

Group Art Unit: 2121

Examiner: BARNES,  
Crystal J.

Atty Docket No.:

**BULLMAN 6-3-3-5**

Our Ref.: 20-164

IN RE PATENT APPLICATION OF:

**BULLMAN 6-3-3-5**

TITLE: **LINK INTEGRITY DUALITY FOR NETWORK DEVICES IN COLD POWER  
STATE**

October 14, 2004

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Sir:

Responsive to the Office Action dated July 19, 2004, please amend  
the subject application as follows:

**IN THE SPECIFICATION:**

Please replace the paragraph starting at page 8, line 25, with the following  
amended paragraph:

In Fig. 1, the hardware link integrity module **100** is shown as being  
permanently tied to D3 power, while the core memory is powered from a system  
power source through a power mode control module **108**. When the network  
device **150** is powered down to a D3 type cold state, system power is removed  
from the core memory **104** (albeit after an arranged power down procedure has  
been performed), but the D3 power source remains. In this way, the hardware  
link integrity module **100** continues to provide link integrity information to other